Semester-VI

Name of the course POWER SYSTEM-II				
Cour	se Code: PC-EEE-601	Semester: 6th		
Dura	tion: 6 months	Maximum Marks: 10	0	
Teach	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutor	ial: 0hr/week	Assignment & Quiz:	10 Marks	
Credi	t Points: 3	Attendance:	05 Marks	
		End Semester Exam:	70 Marks	
Objec		<u> </u>		
1.	To understand the method of representation of	t power system compo	nents	
2.	To know about loacation and components of a	a distribution substation.		
3.	To understand different methods of load flow	studies.		
4.	To determine faults in Electrical systems.	-4 -1.:1:4		
5.	To understand the principle of power systems	stability.		
0.	To understand the principle of relays and met	inods of protection of po	wer system	
/. Dro D	aquisite	iuicu.		
1	Electric Circuit Theory (PC-EE-301)			
2	Electromagnetic field theory (PC-EE-303)			
2.	Power system-I (PC-FF-502)			
Unit	Content		Hrs	Marks
1	Representation of Power system com	onents: Single-phase	III5	TTUIKS
-	representation of balanced three phase n	etworks the one-line		
	diagram and the impedance or reactance of	diagram per unit (PU)	02	
	system.			
	Distribution substation: Types of subs	stations, location of		
	substations, substation equipments and	accessories, earthling	06	
2	(system & equipment), feeder and distribution	utors, radial and loop		
	systems.			
	Load flow studies: Network model formulati	ion, formation of Ybus,		
	load flow problem, Gauss-Siedel meth	od, Newton-Raphson	06	
	method, Decoupled load flow studies, con	nparison of load flow		
3	methods.			
	Faults in Electrical systems: Transient on a t	ransmission line, short		
4	circuit of a synchronous machine under no lo	ad & loaded condition.	08	
	Symmetrical component transformation, sec	quence impedance and		
	sequence network of power system, s	ynchronous machine,		
	transmission lines and transformers. Syr	nmetrical component		
	analysis of unsymmetrical faults, single line-to	o –ground fault, lineto-		
	line fault, double line-to- ground fault			
	Power system stability: Steady state stabil	lity, transient stability,		
	equal area criteria, swing equation, multi ma	chine stability concept	04	

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(Applicable from the academic session 2018-2019)

6 Power system protection: Protective zones, Relaying elements and 14 quantities. Protective relays, basic requirements and type of
protection, phase and amplitude comparator, grading (time & current), classification of Electromagnetic relays, Directional relay, Distant relay, Differential relay, basic aspects of static and digital relays, relay protection scheme for transformer, feeder, generators and motors. Circuit breakers, circuit breaking transients, transient recovery voltage, current chopping and resistance switching, circuit breaker rating, arc and arc extinction, circuit breaker types, oil circuit breaker, vacuum circuit breaker, air blast circuit breaker, SF6 circuit breaker and operating mechanism, advantages and disadvantages of different types.

Text book:

- 1. Modern Power System Analysis, D.P. Kothari & I.J. Nagrath, 4th Edition, Tata McGraw Hill.
- 2. Electrical Power Systems, Subir Ray, PHI
- 3. Switchgear protection and power systems, Sunil S Rao, Khanna Publications.
- 4. A text book on Power System Engineering, M.L.Soni, P.V.Gupta, U.S. Bhatnagar & A. Chakrabarti, Dhanpat Rai & CO.

Reference Books:

- 1. Protection & Switchgear, B. Bhalja, R.P. Maheshwari, N.G.Chothani, Oxford.
- 2. Power system protection & switchgear, B.Ram & D.N. Vishwakarma, Tata McGraw Hill.
- 3. Handbook of Electrical Power Distribution, G. Ramamurthy, University Press
- 4. Electric Power Transmission and Distribution, S. Sivanagaraju, S.Satyanarayana, Pearson Education.
- 5. Power Systems Stability, Vol. I, II & II, E.W. Kimbark, Wiley.
- 6. Power Engineering, D.P Kothari & I.J. Nagrath, Tata McGraw Hill.
- 7. Power Systems Analysis, A. R. Bergen & V. Vittal, Pearson Education. 8. Computer Aided Power systems analysis, Dr. G. Kusic, CEC press.

Course Outcome:

After completion of this course, the learners will be able to

- 1. represent power system components in line diagrams.
- 2. determine the location distribution substation.
- 3. determine the performance of power system with the help of load flowv studies.
- 4. analyse faults in Electrical sysyems.
- 5. determine the stabilty of Power system.
- 6. explain principle of operation of different power system protection equipments.
- 7. solve numerical problems related to representation, load flow, faults, stabilty and protection of power system.

Special Remarks (if any)

Name of the course		MICROPROCESSO	R & MICRO	
Course Code: PC-EEE-602 Semester		Semester: 6th		
Duration: 6 months Maximum Marks		Maximum Marks: 10	0	
2			• •	
Teach	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutor	al: 0hr/week	Assignment & Quiz:	10 Marks	
Credi	t Points: 3	Attendance:	05 Marks	
		End Semester Exam:	70 Marks	
Obje	ctive:			
1.	To understand the Architecture of 8086 micro	processor.		
2.	To learn the design aspects of I/O and Memor	y Interfacing circuits.		
3.	To interface microprocessors with supporting	chips.		
4.	To study the Architecture of 8051 microcontro	oller.		
5.	To design a microcontroller based system			
Pre-F	lequisite			
1.	Analog Electronics (PC-EE-302)			
2.	Digital Electronics (PC-EE-402)		1	1
Unit	Content		Hrs	Marks
1	The 8086 Microprocessor: Introduction to 80	86- Microprocessor		
	architecture – Addressing modes – Instruct	ion set and assembler	08	
	directives – Assembly language progr	amming – Modular		
	Programming – Linking and Relocation – S	Stacks – Procedures –		
	Macros – Interrupts and interrupt service rou	itines – Byte and String		
	Manipulation.			
	8086 System bus structure: 8086 signals -	Basic configurations –		
	System bus timing –System design using 808	6 – I/O programming –		
2	Introduction to Multiprogramming – Syst	tem Bus Structure –	08	
	Multiprocessor configurations – Coprocesso	r, Closely coupled and		
	loosely Coupled configurations – Introc	duction to advanced		
	processors.			
	I/O INTERFACING: Memory Interfacing and I/	O interfacing – Parallel		
	communication interface – Serial communic	cation interface – D/A		
	and A/D Interface – Timer – Keyboard	/display controller -	08	
3	Interrupt controller –DMA controller -	 Programming and 		
	applications Case studies: Traffic Light cont	rol, LED display , LCD		
	display, Keyboard display interface and Alarm	n Controller.		
	Microcontroller: Architecture of 8051	– Special Function		
4	Registers(SFRs) – I/O Pins Ports and Circui	ts – Instruction set –	08	
	Addressing modes – Assembly language prog	ramming.		
<u> </u>	Interfacing Microcontroller: Programming	8051 Timers – Serial		
	Port Programming – Interrupts Programming	ng – LCD & Kevboard	06	
5				

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Interfacing – ADC, DAC & Sensor Interfacing – External Memory	
Interface- Stepper Motor and Waveform generation – Comparison	
of Microprocessor, Microcontroller, PIC and ARM processors	

Text books:

- 1. Advanced Microprocessors and Peripheral, Koshor M Bhurchandi, Ajay Kumar Ray, 3rd Edition, MC Graw hill education.
- 2. Microprocessor & Interfacing, D.V. Hall, Mc Graw Hill.
- 3. The 8051 microcontroller, Ayala, Thomson.

Ref erence books:

- 1. Advanced Microprocessors, Y. Rajasree, New Age international Publishers.
- 2. An introduction to the Intel family of Microprocessors, James L. Antonakos, Pearson Education,
- 3. The 8051 Microcontroller and Embedded systems, Muhammad Ali Mazidi & J. G. Mazidi, Pearson Education.
- 4. The 8086 Microprocessors: Programming & Interfacing the PC, K.J.Ayala, Thomson.
- 5. Microprocessor & Peripherals, S.P. Chowdhury & S. Chowdhury, Scitech.
- 6. Microchip technology data sheet, www.microchip.comerence books

Course Outcome:

After completion of this course, the learners will be able to

- 1. explain the architecture of 8086 and 8051
- 2. do assembly language programming of 8086, 8051
- 3. interface different peripheral with 8086 and 8051
- 4. develop micro processor/ microcontroller based systems
- 5. compare microprocessor, microcontroller, PIC and ARM processors

Special Remarks (if any)

Name of the course NANO ELECT		NANO ELECTRONI	CS	
Cour	se Code: PE-EEE-601A	Semester: 6th		
Dura	tion: 6 months	Maximum Marks: 10	0	
Teacl	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutor	ial: 0hr/week	Assignment & Quiz: 10 Marks		
Credi	t Points: 3	Attendance: 05 Marks		
		End Semester Exam:	70 Marks	
Obje	ctive:			
1.	To understand various aspects of nano-techno	logy		
2.	To understand processes involved in making	nano components and m	aterial	
3.	To understand the concepts of silicon MOSFET and Quantum Transport Devices.			
4.	To understand the fundamental of the device	ces such as logic device	es, field effect	devices, and
	spintronics.			
Pre-F	Requisite			
1.	Analog Electronis (PC-EEE302)			
2.	Digital Electronics(PC-EEE402)			
Unit	Content		Hrs	Marks
1	Introduction to nanotechnology, meso	structures, Basics of		
	Quantum Mechanics: Schrodinger equatio	n, Density of States.		
	Particle in a box Concepts, Degeneracy. B	and Theory of Solids.	10	
	Kronig-Penny Model. Brillouin Zones.			
	Shrink-down approaches: Introduction,	CMOS Scaling, The		
	nanoscale MOSFET, Finfets, Vertical MOSF	ETs, limits to scaling,	14	
2	system integration limits (interconnect issues	etc.).		
	Resonant Tunneling Diode, Coulomb dots	, Quantum blockade,		
	Single electron transistors,Carbon na	anotube electronics,	14	
	Bandstructure and transport, devices	s, applications, 2D		
3	semiconductors and electronic devices,	Graphene, atomistic		
	simulation			

Text book:

- 1. Fundamentals of Nanoelectronics, G.W. Hanson, Pearson, 2009.
- 2. Nanosystems, K.E. Drexler, Wiley, 1992
- 3. Introduction to Nanotechnology, C.P. Poole, F. J. Owens, Wiley, 2003

Reference books

- 1. Nanoelectronics and Information Technology (Advanced Electronic Materialand Novel Devices), W. Ranier, Wiley-VCH, 2003.
- 2. The Physics of Low-Dimensional Semiconductors, J.H. Davies, Cambridge University Press, 1998.

Course Outcome:

After completion of this course, the learners will be able to

- 1. explain various aspects of nano-technology and the processes involved in making nano components and material
- 2. apply the nano-materials in solving practical problems
- 3. describe the types, synthesis, interconnects and applications of carbon nano tubes
- 4. describe the concepts of silicon MOSFET and Quantum Transport Devices
- 5. explains the fundamental of the devices such as logic devices, field effect devices, and spintronics

Special Remarks (if any)

Name of the course		ELECTRICAL MACHINE DESIGN		
Course Code: PE-EEE-601B		Semester: 6th		
Duration: 6 months		Maximum Marks: 10	0	
Teacl	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutor	ial: 0 hr/week	Assignment & Quiz: 1	10 Marks	
Credi	t Points: 3	Attendance: (05 Marks	
		End Semester Exam: 7	70 Marks	
Obje	ctive:			
1.	To understand the baisc principle of design of	Electric machines.		
2.	To understand basics of design of Transformer	r, Induction machine and	d Synchronous r	nachines.
3.	To understand different factors that influence of	design of Electric machi	nes.	
4.	To undertand the need and use software tools	for design of Electric n	nachines	
5.	To solve numerical problems on the topics stud	died		
Pre-F	Requisite			
1.	Electric Machine-I (PC-EE-401)			
2.	Electric Machine-II (PC-EE-501)			
Unit	Content		Hrs	Marks
1	Introduction: Major considerations in Electr	rical Machine Design -		
	Electrical Engineering Materials – Space facto	or – Choice of Specific		
	Electrical and Magnetic loadings - Thermal	considerations - Heat	04	
	flow – Temperature rise and Insulating N	Vaterials - Rating of		
	machines – Standard specifications.			
	Transformer: Output Equations – Main Dimen	nsions - kVA output for		
	single and three phase transformers – Win	ndow space factor -	10	
	Design of core and winding - Overall dim	nensions – Operating		
2	characteristics – No load current – T	Temperature rise in		
	Transformers – Design of Tank - Met	hods of cooling of		
	Transformers.	C C		
	Induction motors: Output equation of Ind	uction motor – Main		
3	dimensions – Choice of Average flux density	/ – Length of air gap-	10	
	Rules for selecting rotor slots of squirrel cage	machines – Design of		
	rotor bars & slots – Design of end rings – D	sign of wound rotor –		
	Magnetic leakage calculations – Leakage re	actance of polyphase		
	machines- Magnetizing current - Short circuit current - Operating			
	characteristics- Losses and Efficiency			
	Synchronous machines: Output equations – c	hoice of Electrical and		
Magnetic Loading - Design of salient note machines - Short size			10	
4 ratio - shape of nole face - Armature design - Armature				
	parameters – Estimation of air gan length – D	lesign of rotor – Design		
	of damper winding - Determination of full lo	ad field mmf - Design		
	of field winding – Design of turbo alternators -	- Rotor design		
	Computer aided Design (CAD): Limitatio	- notor design.		
	Computer aided Design (CAD): Limitatio	ons (assumptions) of		

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traditional designs, need for CAD analysis, synthesis and hybrid 05	
methods, design optimization methods, variables, constraints and	
objective function, problem formulation.	

Text book:

- 1. A Course in Electrical Machine Design, A.K. Sawhney, Dhanpat rai and sons.
- 2. Electrical machine design, V. rajini, V.S. Nagarajan, Pearson India education services Pvt. Ltd.
- 3. Computer Aided Design of Electrical Machine, K. M. V. Murthy, B.S. Publications.

Reference books

- 1. Design and Testing of Electrical Machines, M.V.Deshpande, PHI
- 2. Principles of Electrical Machine Design, 3rd Edition, S.K. sen, Oxf-Ibh
- 3. Computer Aided Design of Electrical Equipment, M. Ramamoorthy, East-West Press.

Course Outcome:

After completion of this course, the learners will be able to

- 1. specify the rating of electrical machines with standard specifications.
- 2. explain the principles of electrical machine design and carry out a basic design of an ac machine
- 3. determine the various factors which influence the design: electrical, magnetic and thermal loading of electrical machines
- 4. explain the construction and performance characteristics of electrical machines.
- 5. use software tools to do design calculations.

Special Remarks (if any)

the course	VLSI AND MICRO ELECTRONICS		
ode: PE-EEE601C	Semester: 6th		
: 6 months	Maximum Marks: 10	0	
Teaching Scheme Examination Schem			
hrs/week	Mid Semester Exam: 1	5 Marks	
) hr/week	Assignment & Quiz: 1	10 Marks	
nts: 3	Attendance: (05 Marks	
	End Semester Exam: 7	70 Marks	
:			
understand the concept of VLSI design			
understand the basics of MOS structure			
understand the process of VLSI fabricatio	n		
understand the principle of logic circuit de	esign with hardware desc	cription languag	e
isite			
alog Electronics (PC-EE 302)			
gital Electronics (PC-EE 402)			
Content		Hrs	Marks
Introduction to VLSI Design: VLSI Design Concepts, Moor's Law,			
Scale of Integration (SSI, MSI, LSI, VLSI, ULSI – basic idea only),			
Types of VLSI Chips (Analog & Digital VLSI chips, General purpose,		08	
ASIC, PLA, FPGA), Design principles (Digital VLSI - Concept of			
gularity, Granularity etc), Design Domains	(Behavioral, Structural,		
ysical), Y-Chart, Digital VLSI Design Steps.			
DS structure : E-MOS & D-MOS, Charge	inversion in E-MOS,		
reshold voltage, Flat band voltage, Poter	itial balance & Charge		
lance, Inversion, MOS capacitances.			
ree Terminal MOS Structure: Body effect		1.0	
ur Terminal MOS Transistor: Drain curre	ent, I-V characteristics.	12	
rrent-voltage equations (simple derivation)		
aling in MOSFET: Short Channel Effe	ects, General scaling,		
nstant Voltage & Field scaling			
10S : CMOS inverter, Simple Combination	al Gates - NAND gate		
d NOR Gate using CMOS.			
cro-electronic Processes for VLSI	Fabrication: Silicon		
miconductor Technology- An Overviev	v, Wafer processing,		
idation, Epitaxial deposition, Ion-implantat	ion & Diffusion,	10	
aning, Etching, Photo-lithography – Posit	ive & Negative photo-		
sist.			
sic CMOS Technology – (Steps in fabricatin	g CMOS), Basic n-well		
1OS process, p-well CMOS process, Twin tu	b process, Silicon on		
ulator	• •		
yout Design Rule: Stick diagram with exam	ples, Layout rules.		
	he course pde: PE-EEE601C 6 months Scheme hrs/week hr/week ats: 3 understand the concept of VLSI design understand the basics of MOS structure understand the principle of logic circuit de isite alog Electronics (PC-EE 302) gital Electronics (PC-EE 402) Content roduction to VLSI Design: VLSI Design C ile of Integration (SSI, MSI, LSI, VLSI, UI pes of VLSI Chips (Analog & Digital VLSI c C, PLA, FPGA), Design principles (Digit gularity, Granularity etc), Design Domains /sical), Y-Chart, Digital VLSI Design Steps. DS structure: E-MOS & D-MOS, Charger reshold voltage, Flat band voltage, Poter ance, Inversion, MOS capacitances. ree Terminal MOS Transistor: Drain currer rent-voltage equations (simple derivation ding in MOSFET: Short Channel Effor istant Voltage & Field scaling IOS: CMOS inverter, Simple Combination d NOR Gate using CMOS. cro-electronic Processes for VLSI niconductor Technology- An Overview dation, Epitaxial deposition, Ion-implantat aning, Etching, Photo-lithography – Posit ist. sic CMOS Technology – (Steps in fabricatin IOS process, p-well CMOS process, Twin tu ulator rout Design Rule: Stick diagram with exam	he course VLSI AND MICRO E dd: PE-EEE601C Semester: 6th 6 months Maximum Marks: 100 Scheme Examination Scheme hrs/week Mid Semester Exam: 1 hr/week Assignment & Quiz: 1 its: 3 Attendance: 0 understand the concept of VLSI design End Semester Exam: 1 understand the basics of MOS structure understand the process of VLSI fabrication understand the process of VLSI fabrication understand the process of VLSI fabrication understand the process of VLSI pesign Concepts, Moor's Law, de of Integration (SSI, MSI, LSI, VLSI, ULSI – basic idea only), ess of VLSI Chips (Analog & Digital VLSI chips, General purpose, C, PLA, FPGA), Design principles (Digital VLSI – Concept of gularity, Granularity etc), Design Domains (Behavioral, Structural, <i>is</i> ical), Y-Chart, Digital VLSI Design Steps. Structure: E-MOS & D-MOS, Charge inversion in E-MOS, eshold voltage, Flat band voltage, Potential balance & Charge ance, Inversion, MOS capacitances. rearminal MOS Transistor: Drain current, I-V characteristics. rent-voltage equations (simple derivation) ding in MOSFET: Short Channel Effects, General scaling, stant Voltage & Field scaling IOS: CMOS inverter, Simple Combinational Gates - NAND gate 1 NOR Gate using CMOS. Toroelectronic Processes for VLSI Fabrication: Silicon niconductor Technology- An Overview, Wafer processing, dation	he course VLSI AND MICRO ELECTRONIC ode: PE-EEE601C Semester: 6th 6 months Maximum Marks: 100 Scheme Examination Scheme hrs/week Mid Semester Exam: 15 Marks hr/week Assignment & Quiz: 10 Marks nts: 3 Attendance: 05 Marks understand the concept of VLSI design understand the process of MOS structure understand the process of VLSI fabrication understand the process of VLSI fabrication understand the principle of logic circuit design with hardware description languag site Content Hrs roduction to VLSI Design: VLSI Design Concepts, Moor's Law, 08 le of Integration (SSI, MSI, LSI, VLSI, ULSI – basic idea only), pes of VLSI Chips (Analog & Digital VLSI – basic idea only), pes of VLSI Chegin principles (Digital VLSI – Concept of gularity, Granularity etc), Design Domains (Behavioral, Structural, sical), Y-Chart, Digital VLSI Design Steps. 08 S structure: E-MOS & D-MOS, Charge inversion in E-MOS, eshold voltage, Flat band voltage, Potential balance & Charge ance, Inversion, MOS capacitances. ree Terminal MOS Structure: Body effect ur Terminal MOS Structure: Body effect ur Terminal MOS Structure: Body effect, ist and CMOS, charge inversion in E-MOS, eshold voltage, Flat band voltage, Potential balance & Charge ance, Inversion, MOS capacitances. ree Terminal MOS Structure: Body effect ist cit tonon ultand teposi

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4	Hardware Description Language – VHDL or Verilog Combinational	08	
	& Sequential Logic circuit Design.		

Text book:

- 1. Digital Integrated Circuit, J.M.Rabaey, Chandrasan, Nicolic, Pearson Education.
- 2. CMOS Digital Integrated Circuit, S.M.Kang & Y.Leblebici, TMH.
- 3. Modern VLSI Design, Wayne Wolf, Pearson Education.
- 4. VHDL, Bhaskar, PHI.
- 5. Advance Digital Design Using Verilog, Michel D. Celliti, PHI

Reference books

- 1. Digital Integrated Circuits, Demassa & Ciccone, John Willey & Sons .
- 2. Modern VLSI Design: system on silicon, Wayne Wolf; Addison Wesley Longman Publisher
- 3. Basic VLSI Design, Douglas A. Pucknell & Kamran Eshranghian, PHI
- 4. CMOS Circuit Design, Layout & Simulation, R.J.Baker, H.W.Lee, D.E. Boyee, PHI

Course Outcome:

After completion of this course, the learners will be able to

- 1. explain the principle of design of VLSI circuits
- 2. explain different MOS structure with characteristics
- 3. apply different processes for VLSI fabrication
- 4. use programming language for the design of logic circuits
- 5. draw the stick diagram and layout for simple MOS circuits

Special Remarks (if any)

Name of the course		ELECTRICAL AND HYBRID VEHICLE		
Course Code: PE-EEE-602A		Semester: 6th		
Durat	tion: 6 months	Maximum Marks: 100		
Teach	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutori	al: 0 hr/week	Assignment & Quiz: 1	10 Marks	
Credit	t Points: 3	Attendance: (05 Marks	
		End Semester Exam: 7	70 Marks	
Objec	ctive:			
1.	To understand the basic difference between co	onventional and Hybrid v	vehicles.	
2.	To understand different configuration and con	trol of Electric drives.		
3.	To understand energy storage system in Hybri	id vehicles.		
4.	To understand different energy management s	trategies of Hybrid vehic	cles.	
5.	To solve numerical problems on the topics stu	Idied		
Pre-R	lequisite			
1.	Electric Machine-I (PC-EE-401)			
2.	Electric Machine-II (PC-EE-501)			
Unit	Content		Hrs	Marks
	Introduction: Conventional Vehicles: Basics o	of vehicle performance,		
	vehicle power source characterization, transr	mission characteristics,		
	mathematical models to describe vehicle perf	formance.		
	Introduction to Hybrid Electric Vehicles: H	History of hybrid and		
1	electric vehicles, social and environmental	importance of hybrid	09	
	and electric vehicles, impact of modern c	drive-trains on energy		
	supplies.			
	Hybrid Electric Drive-trains: Basic concep	t of hybrid traction,		
	introduction to various hybrid drive-train to	opologies, power flow		
	control in hybrid drive-train topologies, fuel e	efficiency analysis.		
	Electric Trains: Electric Drive-trains: Basic	c concept of electric		
	traction, introduction to various electric	drivetrain topologies,		
	power flow control in electric drive-train top	ologies, fuel efficiency		
2	analysis.	0 /	10	
	Electric Propulsion unit: Introduction to elec	ctric components used		
	in hybrid and electric vehicles, Configuration	on and control of DC		
	Motor drives, Configuration and control of Ir	nduction Motor drives,		
	configuration and control of Permanent N	Magnet Motor drives.		
	Configuration and control of Switch Reluctan	nce Motor drives. drive		
	system efficiency.			
	Energy Storage: Energy Storage: Introducti	on to Energy Storage		
	Requirements in Hybrid and Electric Vehicles	. Battery based energy	08	
	storage and its analysis. Fuel Cell based e	nergy storage and its		
3	analysis. Super Capacitor based energy sto	prage and its analysis		
	Flywheel based energy storage and its ana	alysis, Hybridization of		

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	different energy storage devices. Sizing the drive system: Matching the electric machine and the internal combustion engine (ICE), Sizing the propulsion motor, sizing the power electronics, selecting the energy storage technology, Communications, supporting subsystems		
4	Energy Management Strategies: Energy Management Strategies: Introduction to energy management strategies used in hybrid and electric vehicles, classification of different energy management strategies, comparison of different energy management strategies, implementation issues of energy management strategies.	06	
5	Case Studies: Design of a Hybrid Electric Vehicle (HEV), Design of a Battery Electric Vehicle (BEV).	05	

Text book:

- 1. Electric and Hybrid Vehicles: Design Fundamentals, Iqbal Hussein, CRC Press.
- 2. Hybrid Electric Vehicles: Principles and Applications with Practical Perspectives, C. Mi, M. A. Masrur and D. W. Gao, John Wiley & Sons,
- 3. Hybrid Electric Vehicles: Energy Management Strategies, Onori Simona, Serrao Lorenzo and Rizzoni Giorgio, Springer.
- 4. Electric and Hybrid Vehicles, T. Denton, Routledge.

Reference books

- 1. Electric Vehicle Technology Explained, James Larminie, John Lowry, Wiley.
- 2. Modern Electric, Hybrid Electric and Fuel Cell Vehicles: Fundamentals, Theory and Design, Mehrdad Ehsani, YimiGao, Sebastian E. Gay, Ali Emadi CRC Press, 2004.

Course Outcome:

After completion of this course, the learners will be able to

- 1. explain the principle of Electric traction
- 2. choose a suitable drive scheme for developing an electric hybrid vehicle depending on resources
- 3. design and develop basic schemes of electric vehicles and hybrid electric vehicles
- 4. choose proper energy storage systems for vehicle applications
- 5. implement different energy management strategies for hybrid vehicle

Special Remarks (if any)

Name of the course PO		POWER QUALITY AND FACTS			
Course Code: PE-EEE-602B		Semester: 6th			
Duration: 6 months Maximum Marl		Maximum Marks: 100)		
Teach	Teaching Scheme Examination Scheme				
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks		
Tutor	al: 0 hr/week	Assignment & Quiz: 1	0 Marks		
Credit	Points: 3	Attendance: ()5 Marks		
		End Semester Exam:	70 Marks		
Objec	tive:				
1.	To understand the characteristics of ac transm	ission and the effect of s	hunt and series	reactive	
	compensation.				
2.	To understand the working principles of FAC	TS devices and their ope	rating character	istics.	
3.	To understand the basic concepts of power qu	ality.	0		
4.	To understand the working principles of devic	es to improve power qua	ality.		
5.	To solve numerical problems on the topics stu	Idied			
Pre-R	Lequisite				
1.	Power system-I (PC-EE-502)				
2.	Control system (PC-EE-503)				
3.	Power Electronics (PC-EE-504)				
Unit	Content		Hrs	Marks	
	Transmission Lines and Series/Shun	t Reactive Power			
	Compensation: Basics of AC Transm	nission. Analysis of			
	uncompensated AC transmission lines. Pa	ssive Reactive Power	r 04		
	Compensation. Shunt and series compensation at the mid-point of				
1	an AC line. Comparison of Series and Shunt Co	ompensation.			
	Thyristor-based Elexible AC Transmission	Controllers (FACTS):			
	Description and Characteristics of Thyristor	-based FACTS devices:			
	Static VAR Compensator (SVC) Thyrist	or Controlled Series			
2	Canacitor (TCSC) Thyristor Controlled Braki	ng Resistor and Single	06		
	Pole Single Throw (SDST) Switch Con	figurations/Modes of			
	Operation Harmonics and control of SVC ar	ngulations/Wodes of			
	Limitor	iu rese. raut current			
	Voltage Source Converter based (FACTS)	controllars: Valtara			
	Source Converters (V(C)) Six Dules V(C) Mult	i pulso and Multi lovel			
	Source Converters (VSC): Six Pulse VSC, Mult	Concernence and Multi-level			
3	Converters, Pulse-width Modulation for VSC	s. Selective Harmonic			
5	Elimination, Sinusoidal PWM and Space	Vector Modulation.	08		
	STATCOM: Principle of Operation, Reactive	Power Control: Type I	00		
	and Type II controllers, Static Synchronous	s Series Compensator			
	(SSSC) and Unified Power Flow Controller	r (UPFC): Principle of			
	Operation and Control. Working principle of I	Interphase Power Flow			
	Controller. Other Devices: GTO Controlled	Series Compensator.			
	Fault Current Limiter.				
	Application of FACTS : Application of FACTS of	devices for power-flow			
	control and stability improvement. Simulati	on example of power			
4	swing damping in a single-machine infinite bu	is system using a TCSC.	04		

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Syllabus for B. Tech in Electrical & Electron	nics Engineering (EEE)
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(Applicable from the academic session 2018-2019)

	Simulation example of voltage regulation of transmission mid-point		
	voltage using a STATCOM.		
	Power Quality Problems in Distribution Systems : Power Quality		
5	problems in distribution systems: Transient and Steady state	04	
	variations in voltage and frequency. Unbalance, Sags, Swells,		
	Interruptions, Wave-form Distortions: harmonics, noise, notching,		
	dc-offsets, fluctuations. Flicker and its measurement. Tolerance of		
	Equipment: CBEMA curve.		
6.	DSTATCOM : Reactive Power Compensation, Harmonics and		
	Unbalance mitigation in Distribution Systems using DSTATCOM and		
	Shunt Active Filters. Synchronous Reference Frame Extraction of	06	
	Reference Currents. Current Control Techniques for DSTATCOM.		
7.	Dynamic Voltage Restorer and Unified Power Quality Conditioner:		
	Voltage Sag/Swell mitigation: Dynamic Voltage Restorer – Working		
	Principle and Control Strategies. Series Active Filtering. Unified		
	Power Quality Conditioner (UPQC): Working Principle. Capabilities		
	and Control Strategies.		

Text book:

1. FACTS Controllers in Power Transmission and Distribution, N K. R. Padiyar, New Age International (P) Ltd. 2007.

Reference books

- 1. Understanding FACTS: Concepts and Technology of FACTS Systems, N. G. Hingorani and L. Gyugyi Wiley-IEEE Press, 1999.
- 2. Reactive Power Control in Electric Systems, T. J. E. Miller, John Wiley and Sons, New York, 1983.
- 3. Electrical Power Systems Quality", R. C. Dugan, McGraw Hill Education, 2012.
- 4. Electric Power Quality, G. T. Heydt, Stars in a Circle Publications, 1991

Course Outcome:

After completion of this course, the learners will be able to

- 1. analyse uncompensated AC transmission line.
- 2. explain the working principles of FACTS devices and their operating characteristics.
- 3. apply FACTS devices for power flow control and stabilty.
- 4. identify different issues of power quality in distribution system.
- 5. apply different compensation and control techniques for DSTATCOM
- 6. explain working principle of dynamic voltage restorer and UPQC

Special Remarks (if any)

Name of the course INDUSTRIAL ELECTRICAL SYSTE		TEMS		
Course Code: PE-EEE-602C Semester: 6t		Semester: 6th		
Duration: 6 months Maximum Marks: 100				
Teach	ling Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutor	al: 0 hr/week	Assignment & Quiz:	10 Marks	
Credit	Points: 3	Attendance:	05 Marks	
		End Semester Exam:	70 Marks	
Objec	:tive:			
1.	To understand the electrical wiring systems fo	r residential, commercia	l and industrial	consumers,
	representing the systems with standard symbol	ls and drawings, SLD.		
2.	To understand various components of industri	al electrical systems.		
3.	To analyze and selec tthe proper size of variou	is electrical system com	ponents.	
4.	To understand methods of automation of Indu	strial Electrical Systems	5	
5.	To solve numerical problems on the topics stu	died		
Pre-R	lequisite			
1.	Power system-I (PC-EE-502)			
2.	Control system (PC-EE-503)			
3.	Power Electronics (PC-EE-504)			
Unit	Content		Hrs	Marks
	Electrical System Components: LT system	n wiring components,		
	selection of cables, wires, switches, distri	bution box, metering	0.0	
	system, Tariff structure, protection component	nts- Fuse, MCB, MCCB,	06	
1	ELCB, inverse current characteristics, symbo	ls, single line diagram		
	(SLD) of a wiring system, Contactor, Isolator,	Relays, MPCB, Electric		
	shock and Electrical safety practices			
	Residential and Commercial Electrical Systen	ns :Types of residential		
	and commercial wiring systems, general ru	les and guidelines for		
	installation, load calculation and sizing of	wire, rating of main		
2	switch, distribution board and protection de	vices, earthing system	08	
	calculations, requirements of commercial	installation, deciding		
	lighting scheme and number of lamps, ea	arthing of commercial		
	installation, selection and sizing of componen	its.		
	Illumination Systems : Understanding various	s terms regarding light,		
	lumen, intensity, candle power, lamp	efficiency, specific		
	consumption, glare, space to height ratio	o, waste light factor,		
3	depreciation factor, various illumination so	chemes, Incandescent		
	lamps and modern luminaries like CFL, LED) and their operation,	06	
	energy saving in illumination systems, design	n of a lighting scheme		
	for a residential and commercial premises, flo	od lighting.		
	Industrial Electrical Systems I: HT c	connection, industrial		
	substation. Transformer selection. Industrial	loads, motors, starting		
4	of motors, SLD, Cable and Switchgear	selection, Lightning	06	

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Syllabus for B. Tech in Electrical & Electronics Engineering (EEE)

(Applicable from the academic session 2018-2019)

	Protection, Earthing design, Power factor correction – kVAR calculations, type of compensation, Introduction to PCC, MCC	
	panels. Specifications of LT Breakers, MCB and other LT panel	
	components.	
	Industrial Electrical Systems II: DG Systems, UPS System, Electrical	
5	Systems for the elevators, Battery banks, Sizing the DG, UPS and	06
	Battery Banks, Selection of UPS and Battery Banks.	
6.	Industrial Electrical System Automation: Study of basic PLC, Role of	
	in automation, advantages of process automation, PLC based	
	control system design, Panel Metering and Introduction to SCADA	06
	system for distribution automation.	

Text book:

- 1. Electrical Wiring, Estimating & Costing, S. L. Uppal and G. C. Garg, Khanna publishers, 2008.
- 2. Electrical Design, Estimating & Costing, K. B. Raina, New age International, 2007.

Reference books

- 1. Electrical estimating and costing, S. Singh and R. D. Singh, Dhanpat Rai and Co., 1997.
- 2. Web site for IS Standards.
- 3. Residential Commercial and Industrial Systems, H. Joshi, McGraw Hill Education, 2008.

Course Outcome:

After completion of this course, the learners will be able to

- 1. represent electrical wiring system for residential, commercial and industrial consumers
- 2. determine the rating of components of residential and commercial electrical systems
- 3. design lighting scheme for a residential and commercial premises
- 4. select transformer, switchgear, protection equipments for industrial electrical systems
- 5. explain methods of automation of Industrial Electrical Systems
- 6. solve numerical problems related to earthing system, lighting scheme, power factor correction

Special Remarks (if any)

Name	e of the course	ARTIFICIAL INTEL	LIGENCE	
Course Code: OE-EEE-601A Semester: 6th		Semester: 6th		
Durat	Duration: 6 months Maximum Marks: 100			
Teach	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutori	ial: 0hr/week	Assignment & Quiz:	10 Marks	
Credit	t Points: 3	Attendance:	05 Marks	
		End Semester Exam:	70 Marks	
Objec	ctive:			
1.	To understand the basic concepts, theories and	d state-of-the-art techniq	ues of artificial	intelligence.
2.	To understand basic concepts and application	ns of machine learning.		
3.	To learn the application of machine learnin medicine, finance etc.	g /A.I algorithms in the	e different field	s of science,
Pre-R	lequisite			
1.	Programmong for problem solving (ES-CS20	1)		
2.	Mathematics (BS-M301			
3.	Data structure and algorithm(OE-EEE-501A)		1	I
Unit	Content		Hrs	Marks
	Introduction: Overview of Artificial intellige	nce- Problems of AI, AI		
	technique, Tic - Tac - Toe problem.			
1	Intelligent Agents: Agents & environment, nature of environment,			
	structure of agents, goal based agents, utility	based agents, learning	00	
	agents.		06	
	Problem Solving: Problems, Problem Space	& search: Defining the		
	problem as state space search, product	ion system, problem		
	characteristics, issues in the design of search	programs.		
	Search techniques: Solving problems by Sear	rching: problem solving		
	agents, searching for solutions; uniform sea	rch strategies: breadth		
	first search, depth first search, depth limite	d search, bidirectional		
	search, comparing uniform search strategies.			
2	Heuristic search strategies: Greedy best-fi	irst search, A* search,		
2	memory bounded heuristic search: local	search algorithms &	12	
	optimization problems: Hill climbing search	n, simulated annealing	12	
	search, local beam search, genetic a	lgorithms; constraint		
	satisfaction problems, local search for	constraint satisfaction		
	problems.			
Adversarial search : Games, optimal decisions & strategies ir				
	games, the minimax search procedure,	alpha-beta pruning,		
	additional refinements, iterative deepening			
	Knowledge & reasoning: Knowledge	representation issues,		
	representation & mapping, approacl	nes to knowledge	05	
3	representation, issues in knowledge represer	ntation		
1				

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4.	Using predicate logic: Representing simple fact in logic, representing instant & ISA relationship, computable functions & predicates, resolution, natural deduction. Probabilistic reasoning [4] Representing knowledge in an uncertain domain, the semantics of Bayesian networks, Dempster-Shafer theory, Fuzzy sets & fuzzy logic	06	
5.	 Natural Language processing: Introduction, Syntactic processing, semantic analysis, discourse & pragmatic processing. Learning: Forms of learning, inductive learning, learning decision trees, explanation based learning, learning using relevance information, neural net learning & genetic learning. Expert Systems: Representing and using domain knowledge, expert system shells, knowledge acquisition 	08	

Text book:

- 1. Artificial Intelligence, K, Knight, E. Rich, S.B. Nair, 3rd Edition TMH
- 2. A classical approach to Artificial Intelligence, M.C. Trivedi, 2nd Edition, Khanna Publishing House, New Delhi
- 3. Introduction to Artificial Intelligence & Expert Systems, D.W. Patterson, PHI
- 4. Artificial Intelligence A Modern Approach, Stuart Russel, Peter Norvig, Pearson

Reference books

- 1. Computational Intelligence, D. Poole, Alan Mackworth, and Randy Goebe, IOUP
- 2. Logic & Prolog Programming, Saroj Kaushik, New Age International
- 3. Expert Systems principle and programming, J.C. Giarranto, Cengage Learing.

Course Outcome:

After completion of this course, the learners will be able to

- 1. explain the concept of knowledge representation and predicate logic and transform the real life information in different representation
- 2. describe state space and its searching strategies
- 3. demonstrate profeiency in applying scientifc method to models of machine learning
- 4. apply the machine learning concepts in real life problems
- 5. demonstrate an ability to share in discussions of AI, its current scope and limitations, and societal implications

Special Remarks (if any)

Name	Name of the course DATA BASE MANAGEMENT SYSTEM		TEM	
Course Code: OE-EEE-601B Semester: 6th				
Dura	tion: 6 months	Maximum Marks: 100		
Teach	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutor	al: 0hr/week	Assignment & Quiz:	10 Marks	
Credi	t Points: 3	Attendance:	05 Marks	
		End Semester Exam:	70 Marks	
Obje	ctive:			1
1.	To understand the different issues involved in	the design and impleme	ntation of a data	base system
2.	To understand the physical and logical databa	ise designs, database mo	deling, relationa	al,
2	hierarchical, and network models	1, 1	1 4 1	
3.	To understand data manipulation language to	query, update, and man	lage a database	
4.	distributed database, and intelligent database.	i as: database security, ii	Server) Data W	Varehousing
-	distributed database, and interrigent database,		Server), Data V	varenousing
5.	To build a simple database system with model	ing, designing, and imp	lementing a DB	MS.
6.	To understand the different issues involved in	the design and impleme	entation database	e system
Pre-R	Requisite	1)		
1.	Programmong for problem solving (ES-CS20)	l)		
Z.	Data structure and algorithm(OE-EEE-501A)		Ll _{ma}	Montra
	Database system architecture: Data	Abstraction Data		IVIALKS
	Independence Data Definition Language (DC	ADSTIDUTION, Data		
	Independence, Data Demittori Language (DL	nshin model notwork	06	
1	model relational and object oriented d	nship model, network		
	constraints data manipulation operations	ata mouers, integrity		
	Relational query languages: Relational algeb	ra Tunle and domain		
	relational calculus SOL3 DDL and DML co	nstructs Open source		
	and Commercial DRMS - MYSOL OPAC	TE DB2 SOLsonvor		
	Polational database design: Domain and	d data dopondoncy	12	
	Armstrong's axioms Normal forms Dong	a data dependency,		
2	Losslessdesign Query processing and ontim	vization: Evaluation of		
	relational algebra expressions. Query equiva	lence loin strategies		
	Query ontimization algorithms	incluce, join strategies,		
3	Storage strategies: Indices B-trees hashing		05	
	Transaction processing: Concurrency control	, ACID property.		
	Serializability of scheduling, Locking and optin	nistic Concurrency	05	
4.	Control schemes, Database recovery.	,		
	Database Security: Authentication. Author	prization and access		
	control, DAC, MAC and RBAC models. Inti	rusion detection, SQL	05	
5.	injection.	, -		
	Advanced topics: Object oriented and object	relational databases,		

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(Applicable from the academic session 2018-2019)

6.	Logical databases, Web databases, Distributed databases, Data	05	
	warehousing and data mining.		

Text book:

- 1. Database Management Systems, R.P. Mahapatra, Govind verma, Khanna Publishing House.
- 2. Database system concepts, Abraham Silberschatz, Henry F. Korth, S. Sudarshan, McGraw-Hill.
- 3. Fundamentals of Database Systems, R. Elmasri and S. B.Navathe, .Pearson Addison wesley

Reference books

- 1. Foundations of Databases, Abiteboul, Richard Hull, Victor Vianu, Addison-Wesley
- Principles of Database and Knowledge Base Systems", Vol 1, J. D. Ullman, Computer Science Press.

Course Outcome:

After completion of this course, the learners will be able to

- 1. write relational algebra expressions for a query and optimize the developed expressions
- 2. design the databases using E R method and normalization
- construct the SQL queries for Open source and Commercial DBMS -MYSQL, ORACLE and DB2
- 4. optimize the execution using Query optimization algorithm
- 5. determine the transaction atomicity, consistency, isolation, and durability
- 6. implement the isolation property, including locking, time stamping based on currency control and serializability of scheduling

Special Remarks (if any)

Name of the course		ANALYTICAL INSTRUMENTATION		
Course Code: OE-EEE-601C		Semester: 6th		
Duration: 6 months Maximum Marks: 100				
Teach	ning Scheme	Examination Scheme		
Theor	y: 3 hrs/week	Mid Semester Exam: 1	5 Marks	
Tutor	al: 0hr/week	Assignment & Quiz:	10 Marks	
Credit	t Points: 3	Attendance:	05 Marks	
		End Semester Exam:	70 Marks	
	-			
Objec	ctive:			
1.	To understand the usefulness of analytical lns	trumentation for industri	ial application.	
2.	To understand the procedure of determination	n of moisture, viscosi	ty and density v	with different
	methods of measurement.	·		
3.	To understand analysis of gas and oxygen v	vith various methods	· 1	• •
4.	To understand analysis of liquid to find differ	ent properties with vario	us practical exp	eriments.
-				
5.	To understand different Spectroscopic me	thods to determine the	e composition,	temperature,
6	density, motion etc.			
6.	To understand Chromatography technique for	separation of mixture.		
Pre-R				
1.	Chemistry-I (BS-CH101)	FE402)		
Linit	Electrical and Electronic Measurement (PC-E	EE403)	Llas	Montra
	Content	Classification types of		IVIAIKS
	Introduction to Analytical Instrumentation:	Lumiditure dry & wat		
	nistrumental methods weasurement of r	turnially. ary & wet	05	
1	bygramater, daw point mater	type, Electrolysis type	05	
1	nygrometer, dew point meter.			
	Moisture: electrical conductivity type, capa	citive method type. IR		
	method, microwave method, crystal oscillato	r method.		
2	Viscosity: Poiseuilles formula. Savbolt's visco	meter, rotameter type		
	viscometer. friction tube viscometer. Searle's	rotating cylinder type.	06	
	Density: pressure head type, buoyancy e	effect type, Gow-Mac		
	densitometer, radioactive type, photoelectric	type, displacer type		
	Gas Analysis: a) Thermal conductivity metho	od. b) Heat of Reaction		
	method.	,		
	Oxygen Analysis: a) Magneto Dynamic instr	rument(Pauling cell) b)	05	
3	Thermomagnetic type or Hot wire type in	nstrument. c) Zirconia		
	oxygen analyzer. d) Mackerth type galvanic	analyzer for dissolved		
	oxygen analysis.	,		
	Liquid analysis: a) Electrodes-Ion selective	e. Molecular selective		
	types- their variations, b) pH analysis; pH ele	ectrodes. circuit for nH		
4.	measurement and applications, c) Conducti	vity cells – standards	05	
	circuits. d)Polarography- apparatus, circuits	and techniques-pulse		

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(Applicable from the academic session 2018-2019)

	polarography, applications e) Colorimetry	
	Spectroscopic Methods: Introduction, Laws relating to absorption	
	of radiation, Molecular Absorption Spectroscopy in UV & VIS	10
	ranges: sources, wavelength selectors, sample container, detectors,	
	Spectrophotometers (Single beam & Dual beam arrangement).	
	Atomic Absorption & Emission spectroscopy: Atomizers, sources,	
	single & dual beam arrangement.	
5	Plasma Spectroscopy: Sequential & Simultaneous multichannel	
5.	Instruments.	
	Atomic X Ray spectrometry: Absorption & diffraction phenomena,	
	sources, detectors, techniques.	
	IR Spectroscopy: sources, monochromators, detectors. IR	
	Spectrometer, FT-IR spectrometers	
	Chromatography: Introduction, basic definitions, some	
6.	relationships.	06
	Gas Chromatography: basic parts, columns, detectors, techniques.	
	LC : types, HPLC: basic parts, sample injection system, column,	
	detectors, Applications	

Text book:

- 1. Principles of Industrial Instrumentation- D.C. Patranabis, Publisher: Tata McGraw Hill
- 2. Principles of Instrumental Analysis- Skoog, Holler, Nieman, Publisher: Thomson Brooks/Cole
- 3. Introduction to Instrumental Analysis-Robert D. Braun, Publisher: Pharma Book Syndicate
- 4. Handbook of Analytical Instruments- R.S. Khandpur, Publisher: Tata McGraw Hil

Reference books:

- 1. Hand book of Analytical Instruments, K.S. Khandpur, McGraw Hill Education.
- 2. Analytical Instrumentation: A Guide to Laboratory, Portable and Miniaturized Instruments, Gillian McMahon, Wiley.

Course Outcome:

After completion of this course, the learners will be able to

- 1. explain usefulness of analytical Instrumentation for industrial application
- 2. determine moisture, viscosity and density with different method of measurement
- 3. analyse gas and oxygen with various methods
- 4. analyse liquid to find different properties with various practical experiments
- 5. apply different Spectroscopic methods to determine the composition, temperature, density, motion etc
- 6. apply Chromatography for separation of mixture

Special Remarks (if any)

Name	e of the course	ECONOMICS FOR H	ENGINEERS	
Cour	Course Code: HM-601 Semester: 6th			
Dura	tion: 6 months	Maximum Marks: 100		
Teacl	ning Scheme	Examination Scheme		
Theory: 3 hrs/week		Mid Semester Exam: 1	5 Marks	
Tutor	ial: 0 hr/week	Assignment & Quiz: 1	10 Marks	
Credi	t Points: 3	Attendance: (05 Marks	
		End Semester Exam: 7	70 Marks	
Obje	ctive:			
1.	To understand the process of economic decis	ion making		
2.	To understand th basic financial management	aspects		
3.	To develop the skills to analyze financial state	ements		
4.	To understand the basic of accounting			
Pre-F	Requisite			
1.	Basic understanding of Engineering processes	8		
Unit	Content		Hrs	Marks
	Economic Decisions Making – Overview, Pr	oblems, Role, Decision		
	making process.			
	Engineering Costs & Estimation – Fixed, Variable, Marginal &			
	Average Costs, Sunk Costs, Opportunity	Costs, Recurring And		
1	Nonrecurring Costs, Incremental Costs, Casl	h Costs vs Book Costs,		
	Life-Cycle Costs; Types Of Estimate, Estima	ting Models - PerUnit	06	
	Model, Segmenting Model, Cost Indexes,	Power-Sizing Model,		
	Improvement & Learning Curve, Benefits.	, see 9,		
	Cash Flow Interest and Equivalence: Ca	sh Flow – Diagrams		
	Categories & Computation Time Value Of M	onev Deht renavment		
	Nominal & Effective Interest	oney, best repayment,		
2	Present Worth Analysis : End-Of-Vear Con	vention Viewnoint Of		
	Economic Analysis Studios Borrowod Mono	vention, viewpoint Of		
	Inflation & Deflation Taxos Economic Crit	oria Applying Present		
	Marth Techniques, Multiple Alternatives	ena, Applying Present	10	
	Worth Lechniques, Multiple Alternatives.			
	Cash Flow & Rate Of Return Analysis – Calc	culations, i reatment of		
	Salvage value, Annual Cash Flow Analysis, Ar	larysis Periods; Internal		
	Rate Of Return, Calculating Rate Of Return	, incremental Analysis;		
	Best Alternative Choosing An Analysis M	iethoa, Future Worth		
	Analysis, Benefit-Cost Ratio Analysis, Sens	itivity And Breakeven		
	Analysis. Economic Analysis In The Public Se	ctor - Quantifying And		
	Valuing Benefits & drawbacks.			

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(Applicable from the academic session 2018-2019)

	Uncertainty In Future Events - Estimates And Their Use In Economic	
	Analysis, Range Of Estimates, Probability, Joint Probability	
3	Distributions, Expected Value, Economic Decision Trees, Risk, Risk	
5	vs Return, Simulation, Real Options.	
	Depreciation - Basic Aspects, Deterioration & Obsolescence,	10
	Depreciation And Expenses, Types Of Property, Depreciation	10
	Calculation Fundamentals, Depreciation And Capital Allowance	
	Methods, Straight-Line Depreciation Declining Balance	
	Depreciation, Common Elements Of Tax Regulations For	
	Depreciation And Capital Allowances.	
	Replacement Analysis - Replacement Analysis Decision Map,	
4	Minimum Cost Life Of A New Asset, Marginal Cost, Minimum Cost	
	Life Problems.	08
	Inflation And Price Change – Definition, Effects, Causes, Price	
	Change With Indexes, Types of Index, Composite vs Commodity	
	Indexes, Use of Price Indexes In Engineering Economic Analysis,	
	Cash Flows that inflate at different Rates.	
	Accounting – Function, Balance Sheet, Income Statement, Financial	
5	Ratios Capital Transactions, Cost Accounting, Direct and Indirect	06
	Costs, Indirect Cost Allocation.	

Text book:

- 1. Engineering Economics, James L.Riggs, David D. Bedworth, Sabah U. Randhawa 4e, McGraw-Hill Education.
- 2. Engineering Economics Analysis, Donald Newnan, Ted Eschembach, Jerome Lavelle, OUP
- 3. Principle of Engineering Economic Analysis, John A. White, Kenneth E.Case, David B.Pratt, Wiley

Reference books

- 1. Engineering Economy, Sullivan and Wicks, Koelling, Pearson
- 2. Engineering Economics, R.Paneer Seelvan, PHI
- 3. Engineering Economics Analysis, Michael R Lindeburg, ,Professional Pub

Course Outcome:

After completion of this course, the learners will be able to

- 1. evaluate the economic theories, cost concepts and pricing policies
- 2. explain the market structures and integration concepts
- 3. apply the concepts of financial management for project appraisal
- 4. explain accounting systems, the impact of inflation, taxation, depreciation
- 5. analyze financial statements using ratio analysis
- 6. explain financial planning, economic basis for replacement, project scheduling, legal and regulatory issues applied to economic investment and project-management problems

Special Remarks (if any)

Name of the course		ELECTRICAL AND ELECTRONICS DESIGN
		LABORATORY
Course	a Cada: DC EEE 691	Somestory 6 th
Course		Semester: 6
Duration: 6 months		Maximum marks:100
Taaahi	ing Calenna	Exercise the set of th
Teach	ing scheme	Examination scheme:
Theory: 1hr/week		Continuous Internal Assessment:40
Tutorial: 0 hr/week		External Assessment: 60
Practio	cal: 4 hrs/week	
Credit	Points:3	
	GROUP A	
1.	Designing a heating element with specified wattage, voltage and ambient temperature.	
2.	Designing an aircore grounding reactor with fault current	specified operating voltage, nominal current and
3.	Designing the power distribution system for a small township	
4.	Designing a double circuit transmission line fo	r a given voltage level and power (MVA) transfer.
5.	Wiring and installation design of a multistoried residential building (G+4.not less than 16	
	dwelling flats with a lift and common pump)	
	GROUP B	
6.	Designing an ONAN distribution transformer.	
7.	Designing a three phase squirrel cage induction motor.	
8.	Designing a three phase wound rotor induction motor.	
9.	Designing a split phase squirrel cage induction	motor for a ceiling fan or a domestic pump.

Applicable from the academic session 2016

10.	Designing a permanent magnet fractional hp servo motor .
	GROUP C
11.	Design the control circuit of a Lift mechanism
12.	Design a controller for speed control of DC machine.
13.	Design a controller for speed control of AC machine.
14.	Electronic system design employing electronic hardware (Analog, Digital, Mixed signal), microcontrollers, CPLDs, and FPGAs, PCB design and layout leading to implementation of an application

Topics to be covered in the Lecture class:

1.	Basic concepts on measurements; Noise in electronic systems; Sensors and signal conditioning circuits; Introduction to electronic instrumentation and PC based data acquisition; Electronic system design, Analog system design, Interfacing of analog and digital systems, Embedded systems,; System assembly considerations	01

Evaluation Method:

- 1. The students would INDIVIDUALLY design the equipment and systems as per specifications provided by the class teacher following established procedures.
- 2. For each student, one item from each of the three groups would be chosen.
- 3. For unspecified items of specification and or specifications of wires, cables etc., data should be taken by students from handbooks and Indian standard.
- 4. Students should spend the allotted periods for carrying out design computations.
- 5. Their attendance shall be recorded.
- 6. Students should maintain a dedicated bound notebook for recording design activities like calculations, formulae used, sketches, flowcharts etc. The notebook should be regularly submitted to the class teacher for review and signature.
- 7. Evaluation would be based on (i) Class attendance (20%), (ii) Design Note Book (30%) (iii) Design Report (30%) (iv) End of semester viva (20%,)

(Formerly West Bengal University of Technology) Syllabus for B. Tech in Electrical & Electronics Engineering (EEE) (Applicable from the academic session 2018-2019)

Institute may develop experiments based on the theory taught in addition to experiments mentioned.

Course outcome: After completion of this course, the learners will be able to

- 1. explain basic concept of measurement, noise in electronic system, sensor and signal conditioning circuits.
- 2. implement PC based data acquisition systems.
- 3. construct circuits with appropriate instruments and safety precautions.
- 4. design heating elements, air core grounding reactor, power distribution system for small township, double circuit transmission line and Electric machines.
- 5. do wiring and installation design of a multistoried residential building with lift and pump.
- 6. design electronic hardware for controller of lift, speed of AC/DC motor, and for an application with analog, digital, mixed signal, microcontroller and PCB.

Name of the course		POWER SYSTEM-II LABORATORY
Course Code: PC-EEE 691		Semester: 6 th
Durat	ion: 6 months	Maximum marks:100
Teach	ing Scheme	Examination scheme:
Theory: 0 hr/week		Continuous Internal Assessment:40
Tutorial: 0 hr/week		External Assessment: 60
Practi	cal: 2 hrs/week	
Credit	t Points:1	
	Laboratory Exp	eriments:
1.	Study on the characteristics of on load time d	elay relay and off load time delay relay.
2.	Test to find out polarity, ratio and magnetizat	ion characteristics of CT and PT.
3.	Test to find out characteristics of	
	(a) under voltage relay	
	(b) earth fault relay.	
4.	Study on DC load flow	
5.	Study on AC load flow using Gauss-seidel method	
6.	Study on AC load flow using Newton Raphson method.	
7.	Study on Economic load dispatch.	
8.	Study of different transformer protection schemes by simulation	
9.	Study of different generator protection schen	nes by simulation

10.	Study of different motor protection schemes by simulation
11.	Study of different characteristics of over current relay.
12.	Study of different protection scheme for feeder.

Institute may develop experiments based on the theory taught in addition to experiments mentioned.

Course outcome: After completion of this course, the learners will be able to

- 1. identify appropriate equipment and instruments for the experiment.
- 2. test the instrument for application to the experiment.
- 3. construct circuits with appropriate instruments and safety precautions.
- 4. validate the characteristics of under voltage relay, over current relay, earth fault relay, on load time delay relay, off load time delay relay, CT and PT.
- 5. validate protection schemes of transformer, generator, motor and feeder.
- 6. apply software tools to find bus voltage, currents and power flows throughout the electrical system.
- 7. work effectively in a team

Name of the course		MICRO PROCESSOR AND MICRO CONTROLLER
Course Code: PC-EEE 692		Semester: 6 th
Durat	ion: 6 months	Maximum marks:100
Teaching Scheme		Examination scheme:
Theory: 0 hr/week		Continuous Internal Assessment:40
Tutorial: 0 hr/week		External Assessment: 60
Practical: 2 hrs/week		
Credit	: Points:1	
	Laboratory Exp	periments:
1.	Programs for 16 bit arithmetic operations for 8086 (using various addressing modes)	
2.	Program for sorting an array for 8086	
3.	Program for searching for a number or charac	cter in a string for 8086
4.	Program for String manipulations for 8086	
5.	Program for digital clock design using 8086.	
6.	Interfacing ADC and DAC to 8086.	
7.	Parallel communication between two microprocessors using 8255.	
8.	Serial communication between two microprocessor kits using 8251.	
9.	Interfacing to 8086 and programming to control stepper motor.	
10.	Programming using arithmetic, logical and bit	manipulation instructions of 8051

11.	Program and verify Timer/Counter in 8051.
12.	Program and verify interrupt handling in 8051.
13.	UART operation in 8051.
14.	Interfacing LCD to 8051.
15.	Interfacing matrix or keyboard to 8051.
16.	Data transfer from peripheral to memory through DMA controller 8237/8257

Institute may develop experiments based on the theory taught in addition to experiments mentioned.

Course outcome: After completion of this course, the learners will be able to

- 1. identify appropriate equipment and instruments for the experiment.
- 2. test the instrument for application to the experiment.
- 3. construct circuits with appropriate instruments and safety precautions.
- 4. program 8086 for arithmatic operation, sorting of array, searching for a number in a string and string manipulation.
- 5. interface ADC/DAC, 8255, 8251 to 8086 and LCD, keyboard to 8051
- 6. program 8051 using arithmatic, logical and bit manipulation instructions of 8051.
- 7. work effectively in a team