Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)
Syllabus for B. Tech in CSE (Data Science)
(Applicable from the academic session 2020-2021)

SEMESTER – III

Analog & Digital Electronics
Code: ESC-301
Contact: 3L

Name of the Course:	**Analog & Digital Electronics**		
Course Code: ESC-301	Semester: III		
Duration: 6 months	Maximum Marks: 100		
Teaching Scheme		**Examination Scheme**	
Theory: 3 hrs./week		Mid Semester exam: 15	
Tutorial: NIL		Assignment and Quiz: 10 marks	
		Attendance: 5 marks	
Practical: hrs./week		End Semester Exam : 70 Marks	
Credit Points:	3		
Objective:			
1	To acquire the basic knowledge of different analog components and their applications		
2	To acquire the basic knowledge of digital logic levels and application of knowledge to understand digital electronics circuits.		
3	To prepare students to perform the analysis and design of various digital electronic circuits		
Pre-Requisite:			
1	Basic Electronics Parts I & II learned in the First year, semesters 1 & 2. Basic BJTs,.		
2	Basic concept of the working of P-N diodes, Schottky diodes,		
3	Basic FETs and OPAMP as a basic circuit component. Concept of Feedback		

Unit	Content	Hrs/Unit	Marks/Unit
1	Different Classes of Amplifiers - (Class-A, B, AB and C - basic concepts, power, efficiency; Recapitulation of basic concepts of Feedback and Oscillation, Phase Shift, Wein Bridge oscillators Astable & Monostable Multivibrators; Schimtt Trigger circuits, 555 Timer.	9	

2	Binary Number System & Boolean Algebra (recapitulation); BCD, ASCII, EBDIC, Gray codes and their conversions; Signed binary number representation with 1's and 2's complement methods, Binary arithmetic, Venn diagram, Boolean algebra (recapitulation); Representation in SOP and POS forms; Minimization of logic	11	
	expressions by algebraic method. Combinational circuits - Adder and Subtractor circuits (half & full adder & subtractor); Encoder, Decoder, Comparator, Multiplexer, De-Multiplexer and Parity Generator		
3	Sequential Circuits - Basic Flip-flop & Latch, Flip-flops -SR, JK, D, T and JK Master-slave Flip Flops, Registers (SISO, SIPO, PIPO, PISO) Ring counter, Johnson counter Basic concept of Synchronous and Asynchronous counters (detail design of circuits excluded), Design of Mod N Counter	10	
4.	A/D and D/A conversion techniques – Basic concepts (D/A :R-2-R only [2L] A/D: successive approximation [2L]) Logic families- TTL, ECL, MOS and CMOS - basic concepts. (2L)	6	

Text book and Reference books:

1. Microelectronics Engineering –Sedra & Smith-Oxford.
2. Analog Electronics, A.K. Maini, Khanna Publishing House (AICTE Recommended -2018)
3. Analog Electronics, L.K. Maheswari, Laxmi Publications (AICTE Recommended -2018)
4. Principles of Electronic Devices & circuits—B L Thereja & Sedha—S Chand
5. Digital Electronics – Kharate – Oxford
6. Digital Electronics – Logic & Systems by J.Bigmell & R.Donovan; Cambridge Learning.
7. Digital Logic and State Machine Design (3rd Edition) – D.J.Comer, OUP
8. Electronic Devices & Circuit Theory – Boyelstad & Nashelsky - PHI
9. Bell-Linear IC & OP AMP—Oxford
10. P.Raja- Digital Electronics- Scitech Publications
11. Morries Mano- Digital Logic Design- PHI
12. R.P.Jain—Modern Digital Electronics, 2/e ,McGraw Hill
13. H.Taub & D.Shilling, Digital Integrated Electronics- McGraw Hill.
14. D.RayChaudhuri- Digital Circuits-Vol-I & II, 2/e- Platinum Publishers
15. Tocci, Widmer, Moss- Digital Systems,9/e- Pearson
16. J.Bignell & R.Donovan-Digital Electronics-5/e- Cenage Learning.
17. Leach & Malvino—Digital Principles & Application, 5/e, McGraw Hill
18. Floyed & Jain- Digital Fundamentals-Pearson.

Course Outcomes:
On completion of the course students will be able to

ESC-301.1 Realize the basic operations of different analog components.

ESC-301.2 Realize basic gate operations and laws Boolean algebra.

ESC-301.3 Understand basic structure of digital computer, stored program concept and different arithmetic and control unit operations.

Data Structure & Algorithm
Code: PCC-CS301
Contacts: 3L

Name of the Course:	**Data Structure & Algorithm**		
Course Code: PCC-CS(DS)301	Semester: III		
Duration: 6 months	Maximum Marks:100		
Teaching Scheme		**Examination Scheme**	
Theory: 3 hrs./week		Mid Semester exam: 15	
Tutorial: NIL		Assignment and Quiz: 10 marks	
		Attendance : 5 marks	
Practical: hrs./week		End Semester Exam :70 Marks	
Credit Points:	3		
Objective:			
1	To learn the basics of abstract data types.		
2	To learn the principles of linear and nonlinear data structures.		
3	To build an application using sorting and searching		
Pre-Requisite:			
1	CS 201 (Basic Computation and Principles of C		
2	M101 & M201 (Mathematics), basics of set theory		

Unit	Content	Hrs/Unit	Marks/Unit
1	Introduction: Basic Terminologies: Elementary Data Organizations, Data Structure Operations: insertion, deletion, traversal etc.; Analysis of an Algorithm, Asymptotic Notations, Time-Space trade off. Searching: Linear Search and Binary Search Technique sand their complexity analysis.	10	
2	Stacks and Queues: ADT Stack and its operations: Algorithms and their complexity analysis, Applications of Stacks: Expression Conversion and evaluation – corresponding algorithms and complexity analysis. ADT queue, Types of Queue: Simple Queue, Circular Queue, Priority Queue; Operations on each types of Queues: Algorithms and their analysis.	9	

3	Linked Lists: Singly linked lists: Representation in memory, Algorithms of several operations: Traversing, Searching, Insertion into, Deletion from linked list; Linked representation of Stack and Queue, Header nodes, Doubly linked list: operations on it and algorithmic analysis; Circular Linked Lists: all operations their algorithms andthe complexity analysis. Trees: Basic Tree Terminologies, Different types of Trees: Binary Tree, Threaded Binary Tree, Binary Search Tree, AVL Tree; Tree operations on each of the trees and their algorithms with complexity analysis. Applications of Binary Trees. B Tree, B+ Tree: definitions, algorithms and analysis	10	
4.	Sorting and Hashing: Objective and properties of different sorting algorithms: Selection Sort, Bubble Sort, Insertion Sort, Quick Sort, Merge Sort, Heap Sort; Performance and Comparison among all the methods, Hashing. Graph: Basic Terminologies and Representations, Graph search and traversal algorithms and complexity analysis.	9	

Text book and Reference books:

1. "Data Structures and Program Design In C", 2/E by Robert L. Kruse, Bruce P. Leung.
2. "Data Structure & Algorithms Using C", 5th Ed., Khanna Publishing House (AICTE Recommended – 2018)
3. "Fundamentals of Data Structures of C" by Ellis Horowitz, Sartaj Sahni, Susan Anderson-freed.
4. "Data Structures in C" by Aaron M. Tenenbaum.
5. "Data Structures" by S. Lipschutz.
6. "Data Structures Using C" by Reema Thareja.
7. "Data Structure Using C", 2/e by A.K. Rath, A. K. Jagadev.
8. "Introduction to Algorithms" by Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Clifford Stein
9. "Data Structures through C" by Yashwant Kanetkar, BPB Publications.
10. "Expert Data Structures with C++" by R.B Patel, Khanna Publishing House

Course Outcomes:

On completion of the course students will be able to

PCC-CS301.1 Differentiate how the choices of data structure & algorithm methods impact the performance of program.

PCC-CS301.2 Solve problems based upon different data structure & also write programs.

PCC-CS301.3 Identify appropriate data structure & algorithmic methods in solving problem.

PCC-CS301.4 Discuss the computational efficiency of the principal algorithms for sorting, searching, and hashing

PCC-CS301.5 Compare and contrast the benefits of dynamic and static data structures implementations.

Computer Organization
Code: PCC- CS302
Contacts: 3L

Name of the Course:	**Computer Organization**	
Course Code: PCC- DS301	Semester: III	
Duration:6 months	Maximum Marks: 100	
Teaching Scheme		**Examination Scheme**
Theory: 3 hrs./week		Mid Semester exam: 15
Tutorial: NIL		Assignment and Quiz : 10 marks
		Attendance: 5 marks
Practical: hrs./week		End Semester Exam: 70 Marks
Credit Points:	3	

Unit	Content	Hrs/Unit	Marks/Unit
1	Basic organization of the stored program computer and operation sequence for execution of a program. Role of operating systems and compiler/assembler. Fetch, decode and execute cycle, Concept of operator, operand, registers and storage, Instruction format. Instruction sets and addressing modes. [7L] Commonly used number systems. Fixed and floating point representation of numbers.[1L]	8	
2	Overflow and underflow. Design of adders - ripple carry and carry look ahead principles. [3L] Design of ALU. [1L] Fixed point multiplication -Booth's algorithm. [1L] Fixed point division - Restoring and non-restoring algorithms. [2L] Floating point - IEEE 754 standard. [1L]	8	
	Memory unit design with special emphasis on		

3	implementation of CPU-memory interfacing. [2L] Memory organization, static and dynamic memory, memory hierarchy, associative memory. [3L] Cache memory, Virtual memory. Data path design for read/write access. [5L]	10	
4.	Design of control unit - hardwired and microprogrammed control. [3L] Introduction to instruction pipelining. [2L] Introduction to RISC architectures. RISC vs CISC architectures. [2L] I/O operations - Concept of handshaking, Polled I/O, interrupt and DMA. [3L]	10	

Text book and Reference books:

1. Mano, M.M., "Computer System Architecture", PHI.
2. Behrooz Parhami "Computer Architecture", Oxford University Press
3. Hayes J. P., "Computer Architecture & Organisation", McGraw Hill,
4. Hamacher, "Computer Organisation", McGraw Hill,
5. N. senthil Kumar, M. Saravanan, S. Jeevananthan, "Microprocessors and Microcontrollers" OUP
6. Chaudhuri P. Pal, "Computer Organisation & Design", PHI,
7. P N Basu- "Computer Organization & Architecture" ,Vikas Pub
8. Rajaraman – "Computer Organization & Architecture", PHI
9. B.Ram – "Computer Organization & Architecture", Newage Publications

Course Outcomes:

On completion of the course students will be able to

PCC-CS302.1 Understand basic structure of digital computer, stored program concept and different arithmetic and control unit operations.

PCC-CS302.2 Understand basic structure of different combinational circuits- multiplexer, decoder, encoder etc.

PCC-CS302.3 Perform different operations with sequential circuits.

PCC-CS302.4 Understand memory and I/O operations.

Introduction to Data Science

Code: PCC- DS301
Contacts: 3L

Name of the Course:	Introduction to Data Science	
Course Code: PCC- DS302	Semester: III	
Duration:6 months	Maximum Marks: 100	
Teaching Scheme		**Examination Scheme**
Theory: 3 hrs./week		Mid Semester exam: 15
Tutorial: NIL		Assignment and Quiz : 10 marks
		Attendance: 5 marks
Practical: hrs./week		End Semester Exam: 70 Marks
Credit Points:	3	

Unit	Content	Hours
1	Introduction: Objective, scope and outcome of the course	1
2	Toolboxes: Python, fundamental libraries for data Scientists. Integrated development environment (IDE). Data operations: Reading, selecting, filtering, manipulating, sorting, grouping, rearranging, ranking, and plotting.	7
3	Descriptive statistics, data preparation. Exploratory Data Analysis data summarization, data distribution, measuring asymmetry. Sample and estimated mean, variance and standard score. Statistical Inference frequency approach, variability of estimates, hypothesis testing using confidence intervals, using p-values.	8
4	Supervised Learning: First step, learning curves, training-validation and test. Learning models generalities, support vector machines, random forest. Examples	7
5	Regression analysis, Regression: linear regression simple linear regression, multiple & Polynomial regression, Sparse model. Unsupervised learning, clustering, similarity and distances, quality measures of clustering, case study.	10
6	Network Analysis, Graphs, Social Networks, centrality, drawing centrality of Graphs, PageRank, Ego-Networks, community Detection.	7
7	Total	40

Mathematics-III (Differential Calculus)
Code: BSC-301
Contacts: 2L

Name of the Course:	**Mathematics-III (Differential Calculus)**	
Course Code: BSC-301	Semester: III	
Duration: 6 months	Maximum Marks: 100	
Teaching Scheme		**Examination Scheme**
Theory:2 hrs./week		Mid Semester exam: 15
Tutorial: NIL		Assignment and Quiz: 10 marks
		Attendance: 5 marks
Practical: NIL		End Semester Exam: 70 Marks
Credit Points:	2	

Unit	Content	Hrs/Unit	Marks/Unit
1	Convergence of sequence and series, tests for convergence, power series, Taylor's series. Series for exponential, trigonometric and logarithmic functions.	8	
2	Limit, continuity and partial derivatives, Chain rule, Implicit function, Jacobian, Directional derivatives, Total derivative; Maxima, minima and saddle points; Gradient, curl and divergence and related problems.	7	
3	Double and triple integrals (Cartesian and polar), change of order of integration in double integrals, Change of variables (Cartesian to polar). Theorems of Green, Gauss and Stokes (Statement only) and related problems.	8	
4.	First Order Differential Equation, Exact, Linear and Bernoulli's equations, Equations of first order but not of first degree: equations solvable for p, equations solvable for y, equations solvable for x	9	
	and Clairaut's form, general & singular solution. [5L] Second order linear differential equations with constant coefficients, D-operator method, method of variation of parameters, Cauchy-Euler equation. [4L]		
5	Basic Concept of graph, Walk, Path Circuit, Euler and Hamiltonian graph, diagraph. Matrix Representation: Incidence & Adjacency matrix. Tree: Basic Concept of tree, Binary tree, Spanning Tree, KrusKal and Prim's algorithm for finding the minimal spanning tree.	8	

Text book and Reference books:

1. Higher Algebra, S. K. Mapa, Levant Books.
2. Advanced Higher Algebra, Chakravorty and Ghosh, U N Dhar Pvt. Ltd.
3. Co-ordinate Geometry, S. L. Loney
4. Integral Calculus, Das and Mukherjee, U N Dhar Pvt. Ltd.
5. Differential Calculus, Das and Mukherjee, U N Dhar Pvt. Ltd.
6. Advanced Engineering Mathematics, E Kreyszig
7. Advanced Engineering Mathematics, Chandrika Prasad & Reena Garg, Khanna Publishing House (AICTE Recommended Textbook -2018)

Economics for Engineers (Humanities-II)
Code: HSMC-301
Contacts: 3L

Name of the Course:	**Economics for Engineers (Humanities-II)**	
Course Code: HSMC-301	Semester: III	
Duration: 6 months	Maximum Marks: 100	
Teaching Scheme		**Examination Scheme**
Theory:3 hrs./week		Mid Semester exam: 15
Tutorial: NIL		Assignment and Quiz: 10 marks
		Attendance: 5 marks
Practical: NIL		End Semester Exam: 70 Marks
Credit Points:	3	

Unit	Content	Hrs/Unit	Marks/Unit
1	1. Economic Decisions Making – Overview, Problems, Role, Decision making process. 2. Engineering Costs & Estimation – Fixed, Variable, Marginal & Average Costs, Sunk Costs, Opportunity Costs, Recurring And Nonrecurring Costs, Incremental Costs, Cash Costs vs Book Costs, Life-Cycle Costs; Types Of Estimate, Estimating Models - Per- Unit Model, Segmenting Model, Cost Indexes, Power-Sizing Model, Improvement & Learning Curve, Benefits.	9	
2	3. Cash Flow, Interest and Equivalence: Cash Flow – Diagrams, Categories & Computation, Time Value of	9	

	Money, Debt repayment, Nominal& Effective Interest. 4. Cash Flow & Rate of Return Analysis – Calculations, Treatment of Salvage Value, Annual Cash Flow Analysis, Analysis Periods; Internal Rate of Return, Calculating Rate of Return, Incremental Analysis; Best Alternative Choosing an Analysis Method, Future Worth Analysis, Benefit-Cost Ratio Analysis, Sensitivity and Breakeven Analysis. Economic Analysis In The Public Sector -Quantifying And Valuing Benefits & drawbacks.		
3	5. Inflation and Price Change – Definition, Effects, Causes, Price Change with Indexes, Types of Index, Composite vs Commodity Indexes, Use of Price Indexes In Engineering Economic Analysis, Cash Flows that inflate at different Rates. 6. Present Worth Analysis: End-Of-Year Convention, Viewpoint Of Economic Analysis Studies, Borrowed Money Viewpoint, Effect Of Inflation & Deflation, Taxes, Economic Criteria, Applying Present Worth Techniques, Multiple Alternatives. 7. Uncertainty In Future Events - Estimates and Their Use in Economic Analysis, Range Of Estimates, Probability, Joint Probability Distributions, Expected Value, Economic Decision Trees, Risk, Risk vs Return, Simulation, Real Options.	9	
4.	8. Depreciation - Basic Aspects, Deterioration & Obsolescence, Depreciation And Expenses, Types Of Property, Depreciation Calculation Fundamentals, Depreciation And Capital Allowance Methods, Straight-Line Depreciation Declining Balance Depreciation, Common Elements Of Tax Regulations For Depreciation And Capital Allowances. 9. Replacement Analysis - Replacement Analysis Decision Map, Minimum Cost Life of a New Asset, Marginal Cost, Minimum Cost Life Problems. 10. Accounting – Function, Balance Sheet, Income Statement, Financial Ratios Capital Transactions, Cost Accounting, Direct and Indirect Costs, Indirect Cost Allocation.	9	

Text book and Reference books:

1. James L.Riggs, David D. Bedworth, Sabah U. Randhawa : Economics for Engineers 4e , Tata McGraw-Hill
2. Donald Newnan, Ted Eschembach, Jerome Lavelle: Engineering Economics Analysis, OUP
3. John A. White, Kenneth E. Case, David B. Pratt : Principle of Engineering Economic Analysis, John Wiley
4. Sullivan and Wicks: Engineering Economy, Pearson
5. R.Paneer Seelvan: Engineering Economics, PHI
6. Michael R Lindeburg : Engineering Economics Analysis, Professional Pub
7. Premvir Kapoor, Sociology & Economics for Engineers, Khanna Publishing House (AICTE Recommended Textbook – 2018)

PRACTICAL
SYLLABUS
Semester III

Analog & Digital Electronics Lab
Code: ESC-391
Contacts: 4P

Name of the Course:	**Analog & Digital Electronics Lab**
Course Code: ESC-391	Semester: III
Duration: 6 months	Maximum Marks: 100
Teaching Scheme:	
Theory: hrs./week	Continuous Internal Assessment
Tutorial: NIL	External Assesement: 60
Practical: 4 hrs./week	Distribution of marks: 40
Credit Points:	2

Laboratory Experiments:	
Analog Electronics	
1	Design a Class A amplifier
2	Design a Phase-Shift Oscillator
3	Design of a Schmitt Trigger using 555 timer
Digital Electronics	
4	Design a Full Adder using basic gates and verify its output / Design a Full Subtractor circuit using basic gates and verify its output.
5	Construction of simple Decoder & Multiplexer circuits using logic gates.
6	Realization of RS / JK / D flip flops using logic gates
7	Design of Shift Register using J-K / D Flip Flop
8	Realization of Synchronous Up/Down counter
9	Design of MOD- N Counter
10	Study of DAC

Any experiment specially designed by the college
(Detailed instructions for Laboratory Manual to be followed for further guidance)

Data Structure & Algorithm Lab
Code: PCC-CS391
Contacts: 4P

Name of the Course:	**Data Structure & Algorithm Lab**
Course Code: PCC-CS(DS)391	Semester: III
Duration: 6 months	Maximum Marks: 100
Teaching Scheme:	
Theory: hrs./week	Continuous Internal Assessment
Tutorial: NIL	External Assesement: 60
Practical: 4 hrs./week	Distribution of marks: 40
Credit Points:	2

Laboratory Experiments:	
Linear Data Structure	
1	Implementation of array operations
2	Stacks and Queues: adding, deleting elements Circular Queue: Adding & deleting elements
3	Merging Problem: Evaluation of expressions operations on Multiple stacks & queues:
4	Implementation of linked lists: inserting, deleting, inverting a linked list. Implementation of stacks & queues using linked lists
5	Polynomial addition, Polynomial multiplication
Non Linear Data Structure	
6	Recursive and Non-recursive traversal of Trees
7	Threaded binary tree traversal. AVL tree implementation
8	Application of Trees. Application of sorting and searching algorithms
9	Hash tables implementation: searching, inserting and deleting, searching & sorting techniques.

Any experiment specially designed by the college
(Detailed instructions for Laboratory Manual to be followed for further guidance)

Computer Organization Lab
Code: PCC- CS392
Contacts: 4P

Name of the Course:	**Computer Organization Lab**
Course Code: PCC-CS(DS)392	Semester: III
Duration:6 months	Maximum Marks: 100
Teaching Scheme:	
Theory: hrs./week	Continuous Internal Assessment
Tutorial: NIL	External Assesement: 60
Practical: 4 hrs./week	Distribution of marks: 40
Credit Points:	2

Laboratory Experiments:	
1	Familiarity with IC-chips: a) Multiplexer, b) Decoder, c) Encoder b) Comparator Truth Table verification and clarification from Data-book.
2	Design an Adder/Subtractor composite unit.
3	Design a BCD adder.
4	Design of a 'Carry-Look-Ahead' Adder circuit.
5	Use a multiplexer unit to design a composite ALU
6	Use ALU chip for multibit arithmetic operation
7	Implement read write operation using RAM IC
8	8. (a) & (b) Cascade two RAM ICs for vertical and horizontal expansion.

Any experiment specially designed by the college
(Detailed instructions for Laboratory Manual to be followed for further guidance)

Data Science Lab using Python
Code: PCC DS391
Contacts: 4P

Name of the Course:	**Data Science Lab using Python**
Course Code: PCC-CS(DS)393	Semester: III
Duration: 6 months	Maximum Marks: 100
Teaching Scheme:	
Theory: NIL	Continuous Internal Assessment
Tutorial: NIL	External Assessment: 60
Practical: 4 hrs./week	Distribution of marks: 40
Credit Points:	2

Practical Syllabus

1. Interactive commands in Python, data operations, simple programs for writing into files and reading from files. Data file manipulations programs.
2. Familiarization with IDE in Python.
3. Writing programs for standard algorithms of sorting and searching in Python.
4. Plotting the data using X-Y graph, Bar- chart, and using other plotting techniques.
5. Write programs to perform exploratory data analysis: variance, standard derivation, summarization, distribution, and statistical inference.
6. Plotting the various distributions for given data sets.
7. Classifying and presentation of data using support vector machine.
8. Write programs for k-means clustering and presentation for given data sets.
9. Write programs on graphs of social networks for community detection.
10. Write programs for analysis of graphs to find centrality and page-rank.

Text book and Reference books:
Dr. Jeeva Jose, Begineer's Guide for Data Analysis Using R Programming, Khanna Publishing House, New Delhi