



**MAULANA ABUL KALAM AZAD UNIVERSITY OF TECHNOLOGY, WEST BENGAL  
ROUTINE FOR 2ND SEM M.TECH ECE (VLSI-NEW) 2019  
COLLEGE-104/105/130/155/209**

<b>DATE</b>	<b>TIME</b>	<b>P CODE</b>	<b>PAPER NAME</b>
06/06/2019	2.00PM-5.00PM	MVLSI-201	Processor Architecture for VLSI
08/06/2019	2.00PM-5.00PM	MVLSI-202	Digital Signal Processing and Application
11/06/2019	2.00PM-5.00PM	MVLSI-203	Analog IC Design
13/06/2019	2.00PM-5.00PM	ELECTIVE	MVLSI-204A/B/C/D
15/06/2019	2.00PM-5.00PM	ELECTIVE	MVLSI-205A/B/C/D/E

**Elective list**

PAPER CODE	PAPER NAME
MVLSI-204A	Quantum & Nano Science
MVLSI-204B	Error Control & Coding
MVLSI-204C	Sensors
MVLSI-204D	Physical Design & Testing
MVLSI-205A	RF Circuits & Systems
MVLSI-205B	Low Power VLSI Design
MVLSI-205C	Mobile Communication
MVLSI-205D	Advanced Micro & Nano Devices
MVLSI-205E	Advanced FET Technology

**Sd/-  
Dr. Subhashis Datta  
(Controller of Examinations)**