



**MAULANA ABUL KALAM AZAD UNIVERSITY OF TECHNOLOGY, WEST BENGAL
ROUTINE FOR 2ND SEM M.TECH ECE (VLSI-NEW) 2018
COLLEGE-103/104/105/130/182/209**

DATE	TIME	P CODE	PAPER NAME
28/05/2018	2.00PM-5.00PM	MVLSI-201	Procesor Architecture for VLSI
30/05/2018	2.00PM-5.00PM	MVLSI-202	Digital Signal Processing and Application
01/06/2018	2.00PM-5.00PM	MVLSI-203	Analog IC Design
04/06/2018	2.00PM-5.00PM	ELECTIVE	MVLSI-204A/B/C/D
06/06/2018	2.00PM-5.00PM	ELECTIVE	MVLSI-205A/B/C/D/E

Sd/-

HAIDER ALI

Controller of Examinations (OSD)

Elective list

PAPER CODE	PAPER NAME
MVLSI-204A	Quantum & Nano Science
MVLSI-204B	Error Control & Coding
MVLSI-204C	Sensors
MVLSI-204D	Physical Design & Testing
MVLSI-205A	RF Circuits & Systems
MVLSI-205B	Low Power VLSI Design
MVLSI-205C	Mobile Communication
MVLSI-205D	Advanced Micro & Nano Devices
MVLSI-205E	Advanced FET Technology